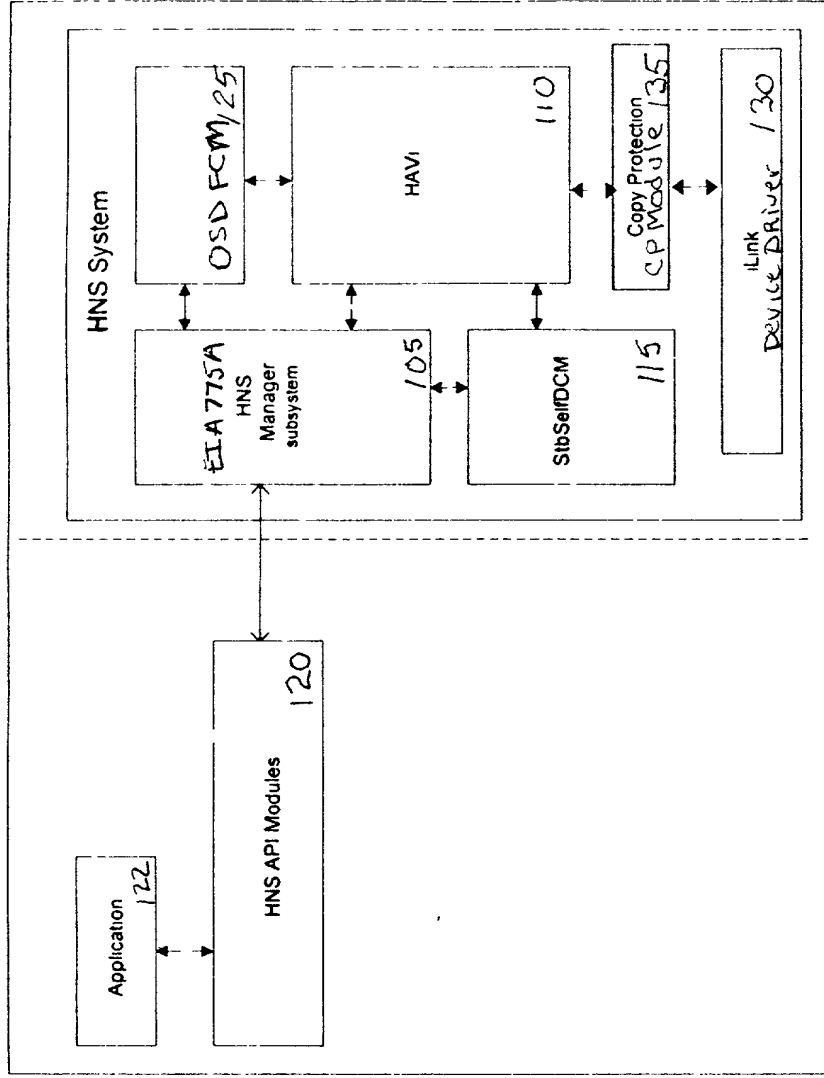


100



System Block Diagram Fig-1

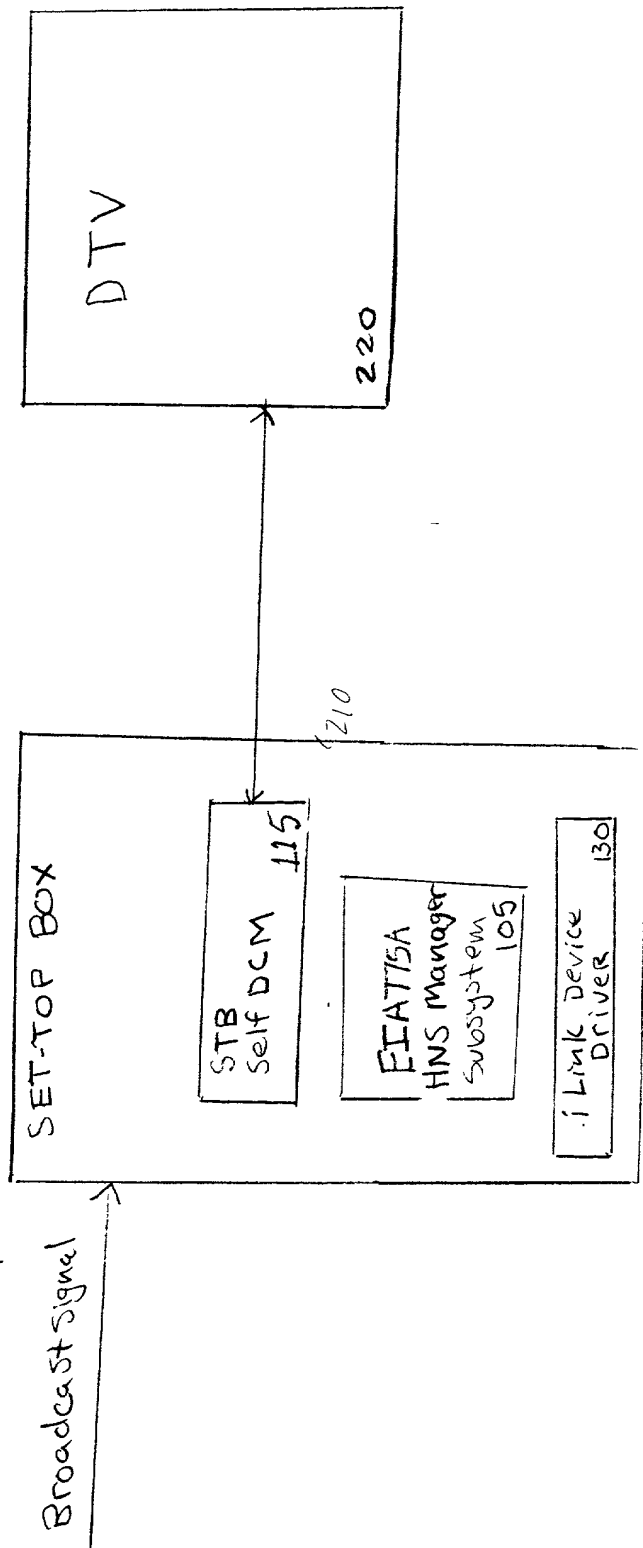
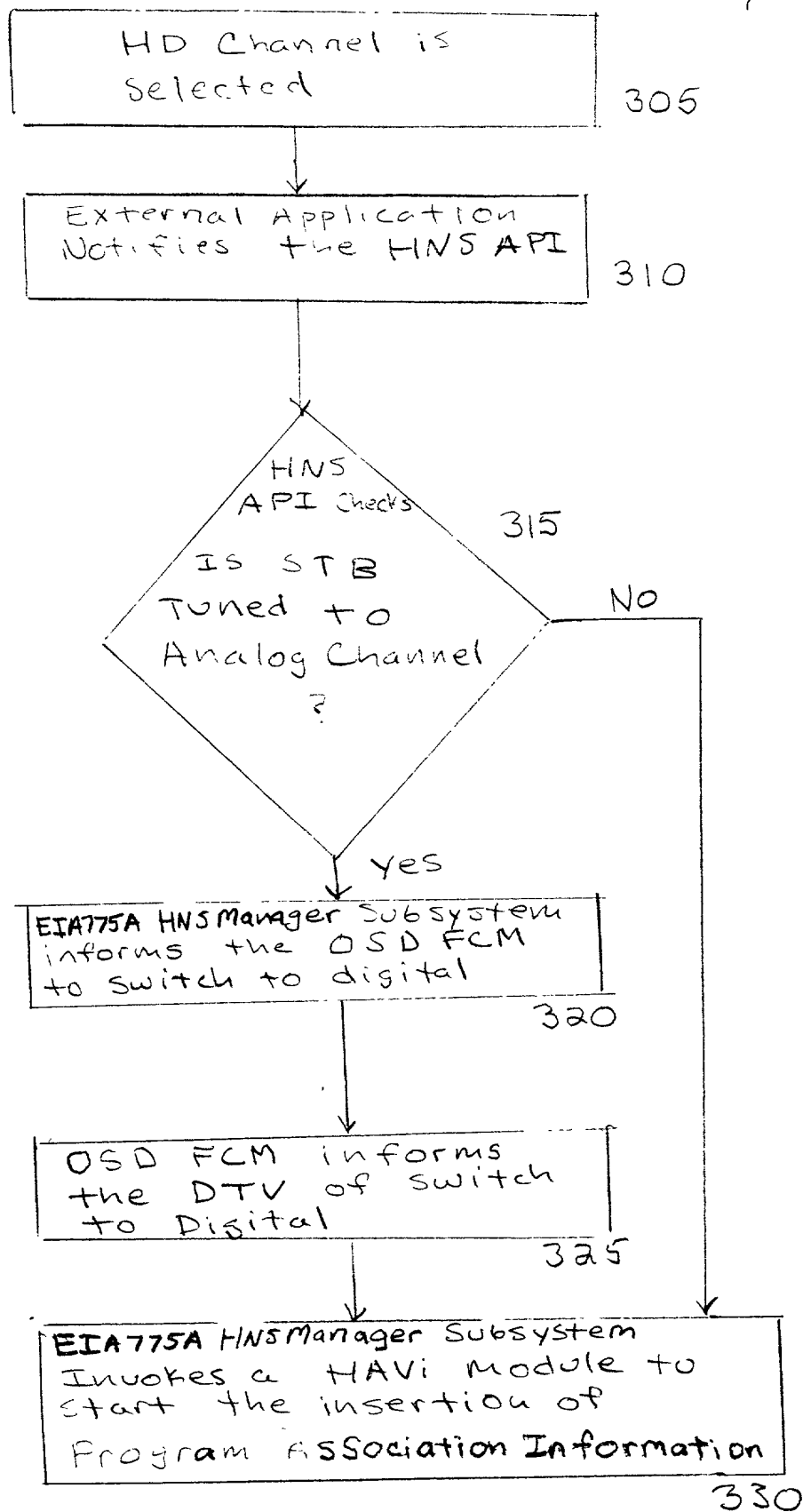


Fig 2

High Definition Receiver
Block Diagram



High Definition
Transmission

Fig. 3

400

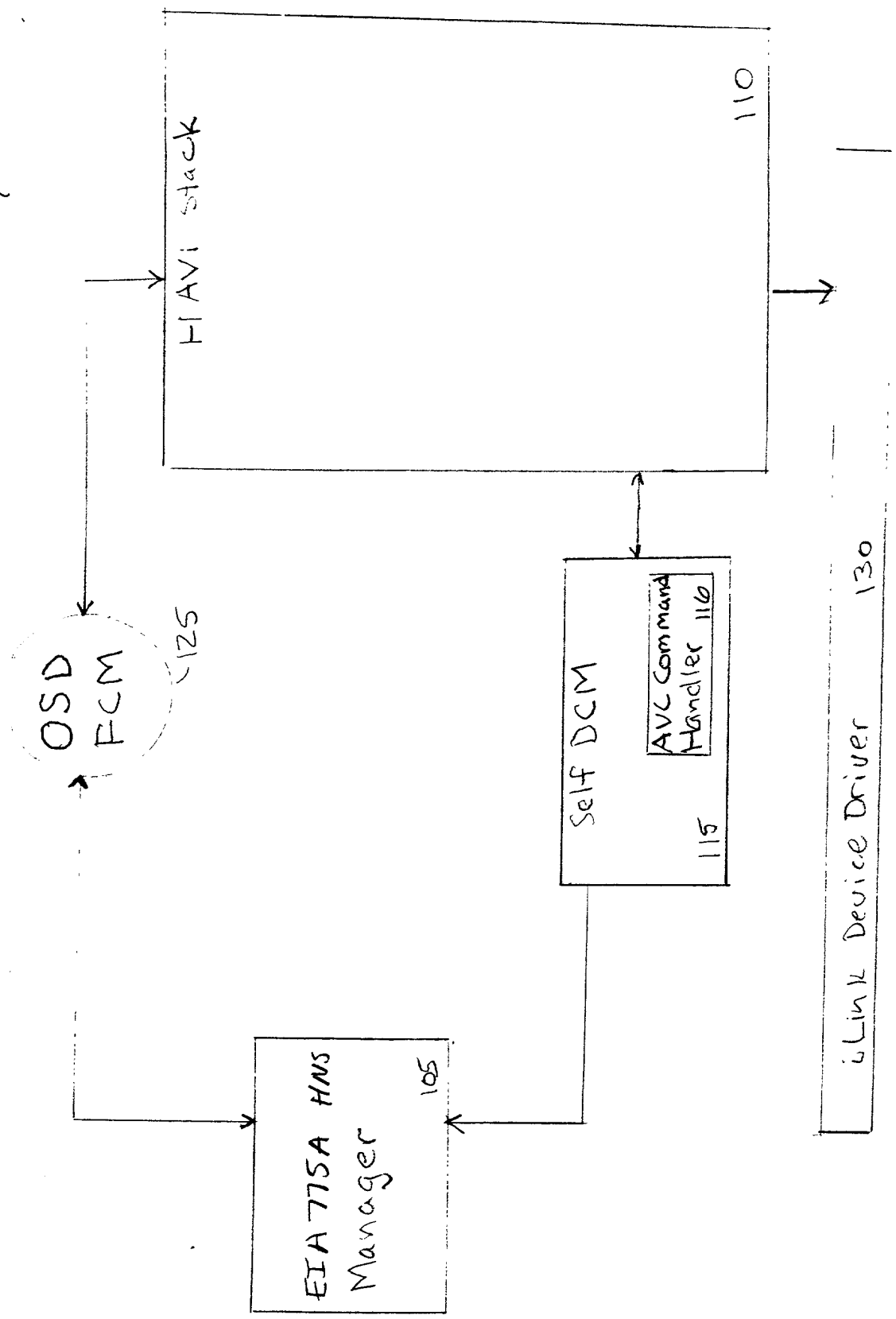


Fig 4

500

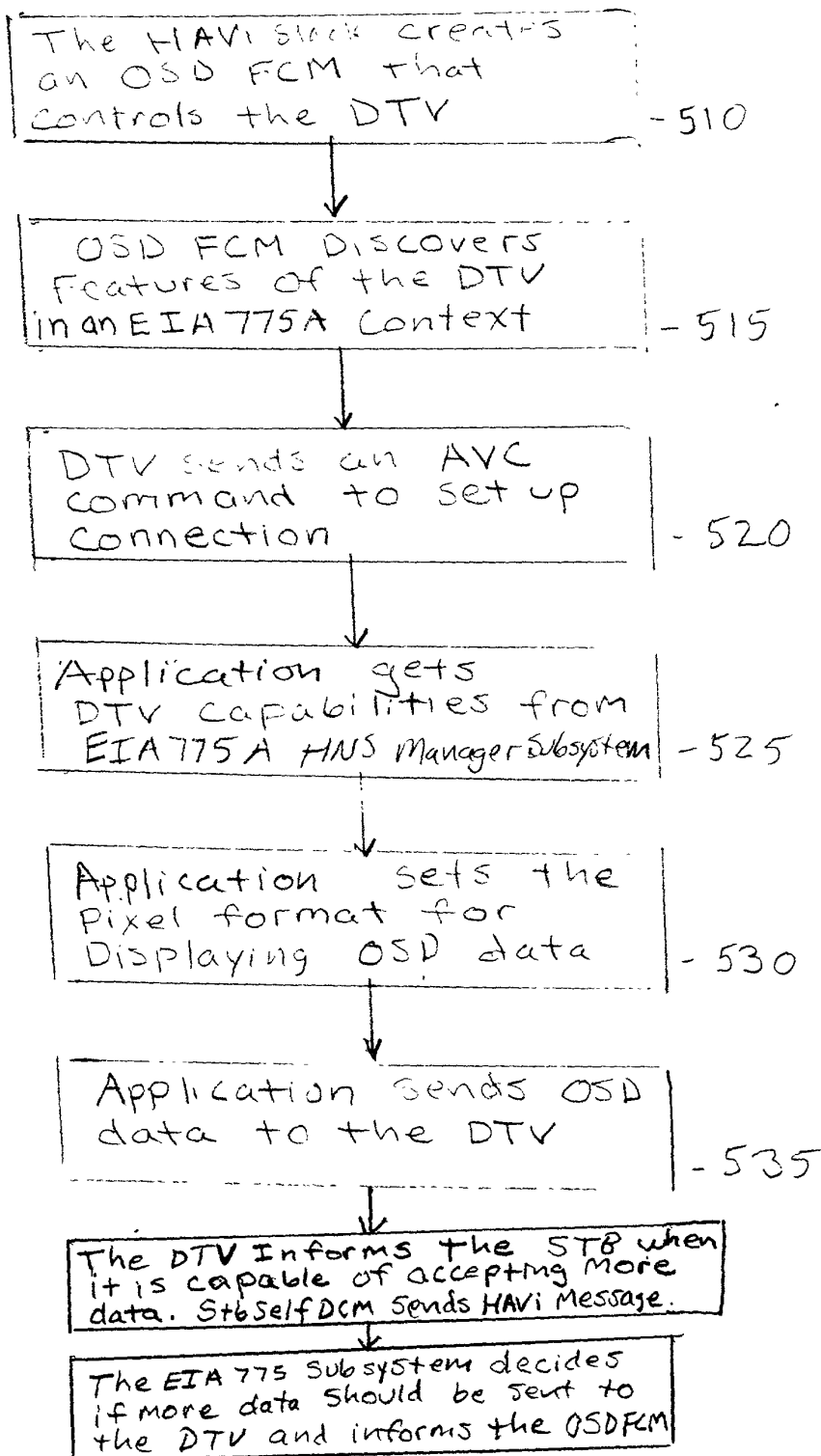


Fig. 5

TOP SECRET

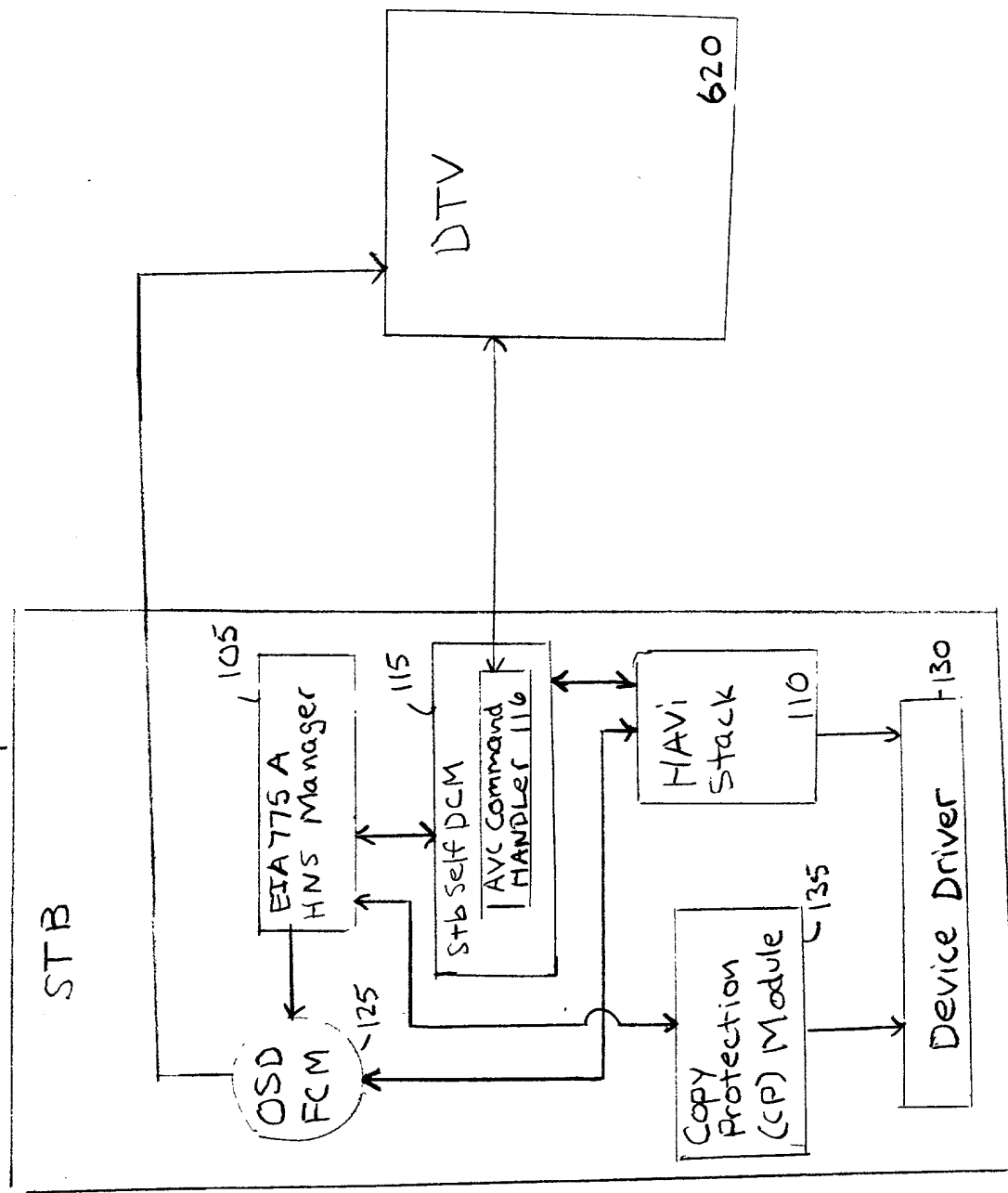


Fig. 6

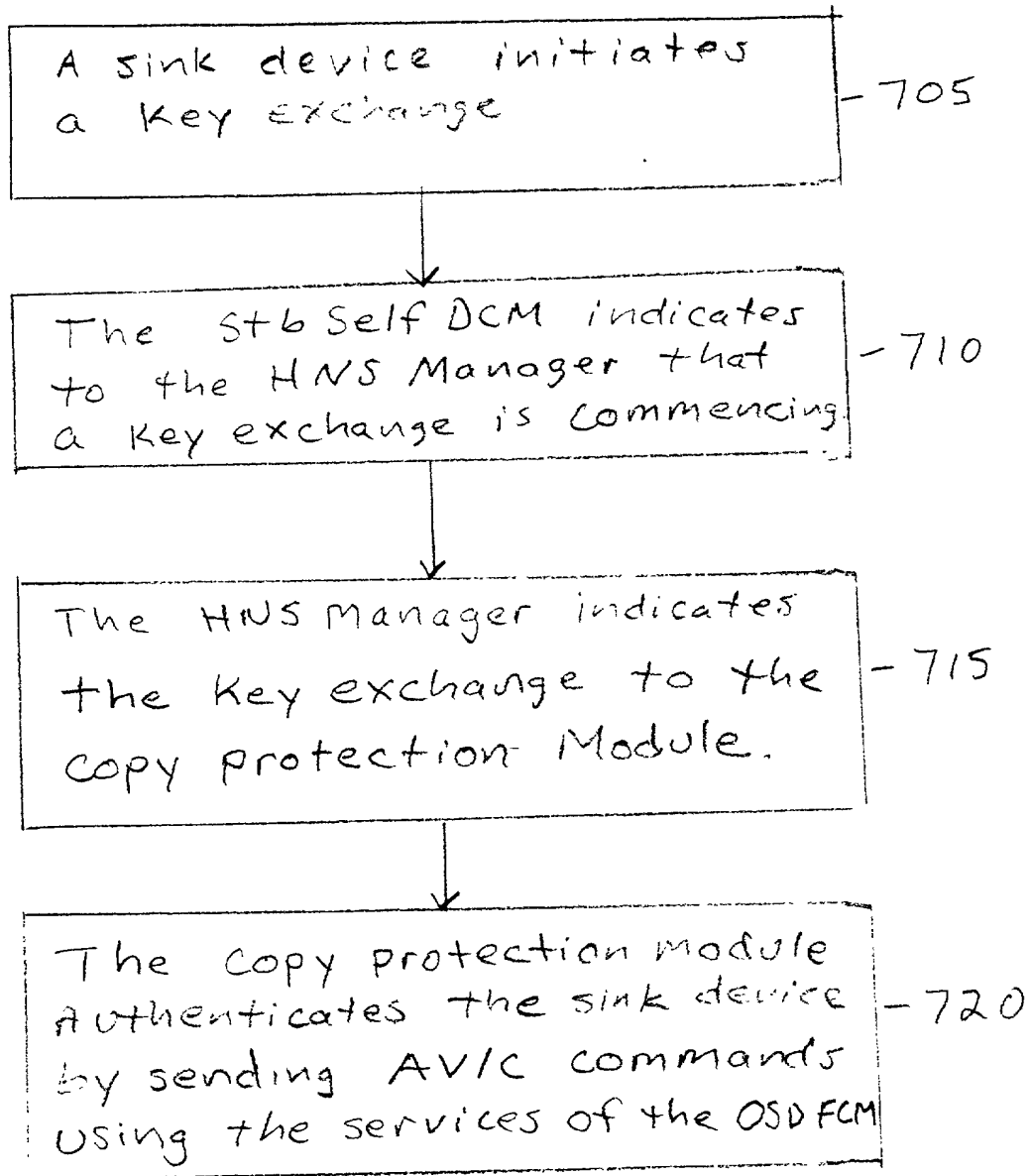


Fig. 7